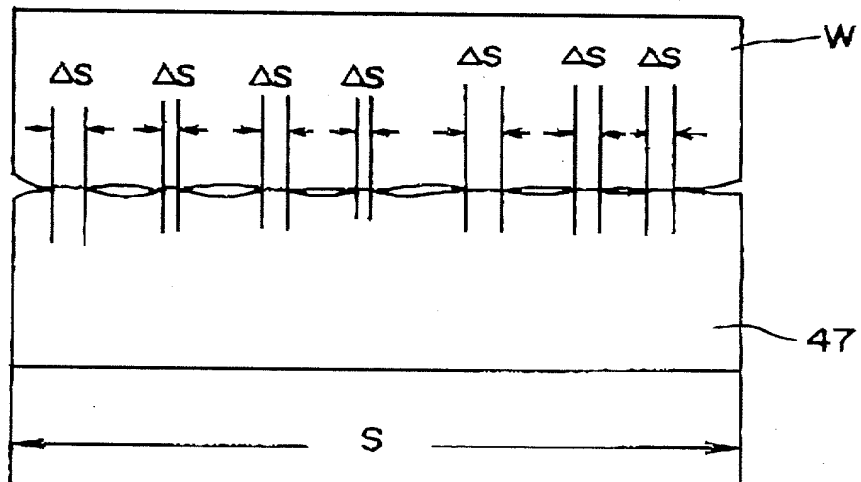


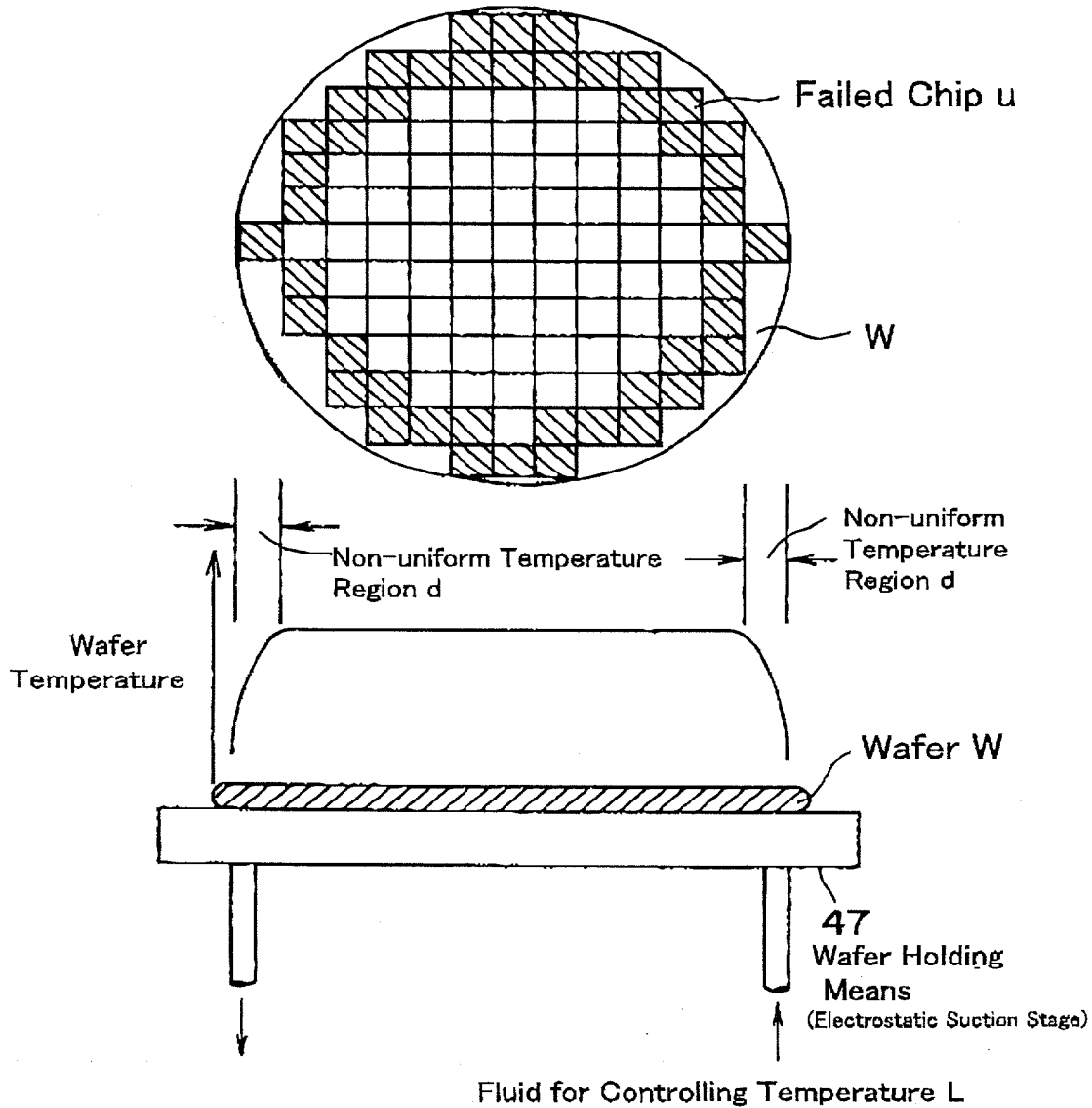
Fig.1



2/11

Fig.2

Temperature Distribution in Wafer Surface
in Device Process Chamber



3/11

Fig 3.(A)

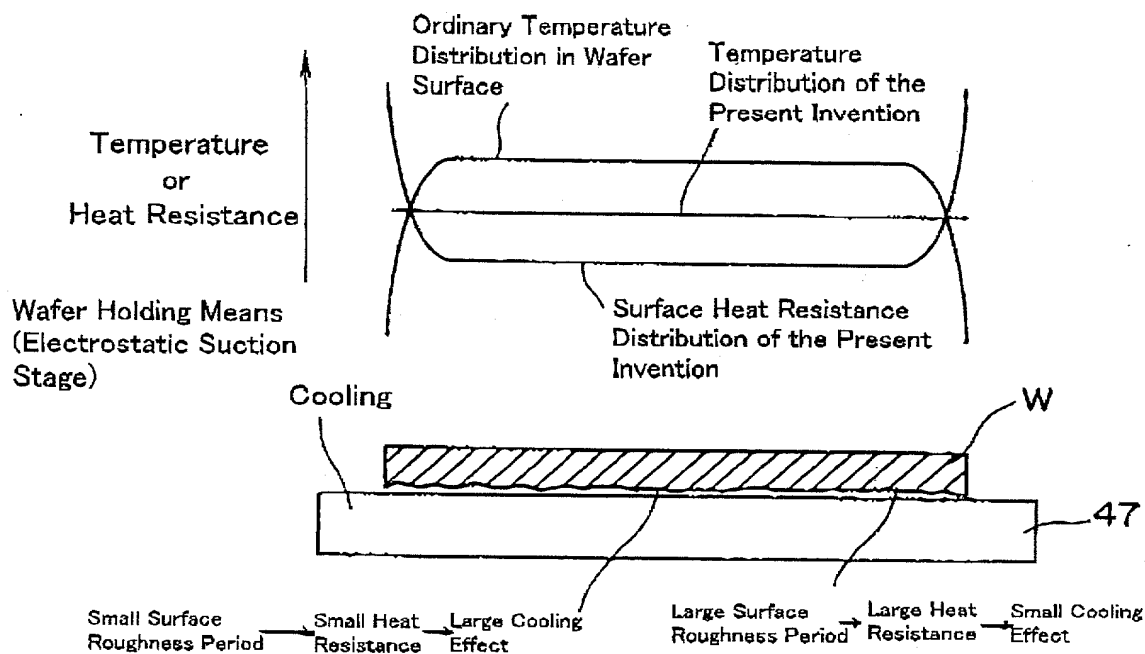
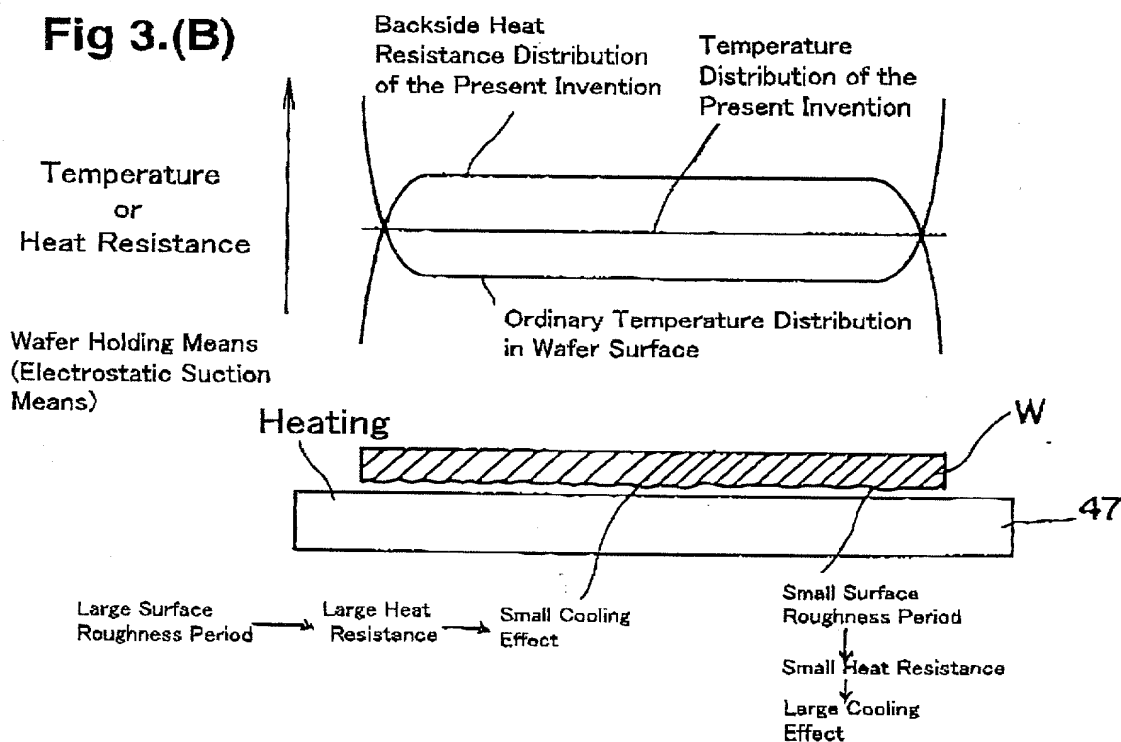
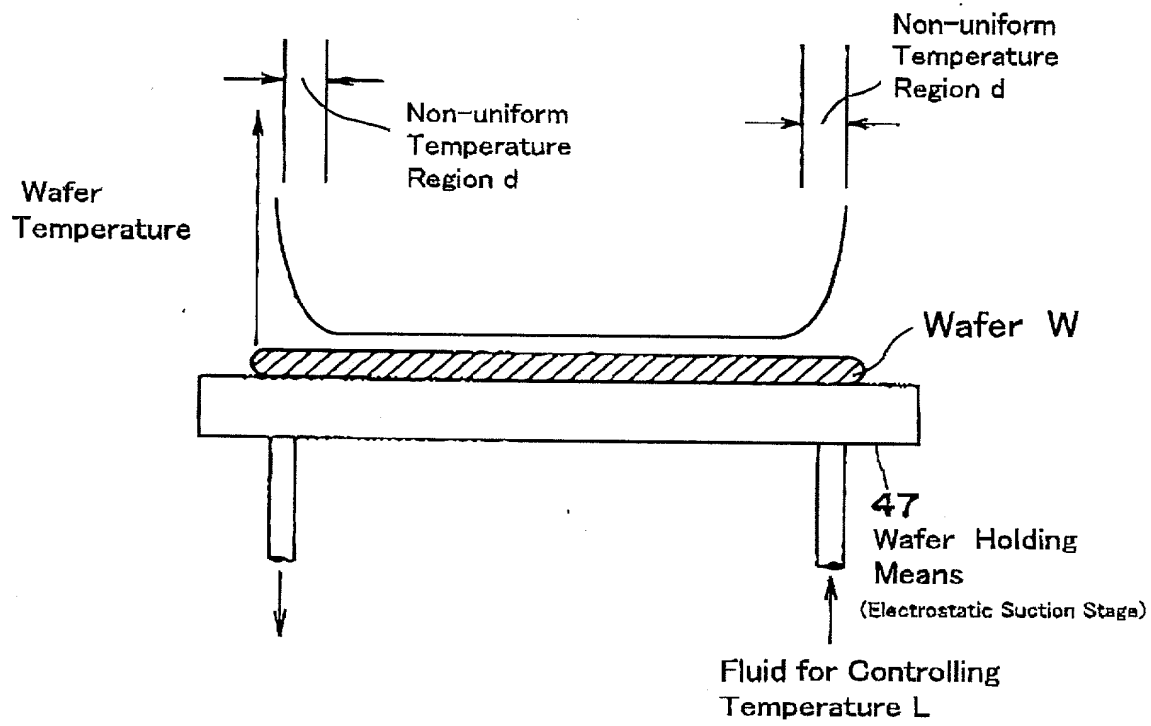


Fig 3.(B)



4/11

Fig.4



5/11

Fig.5 (A)

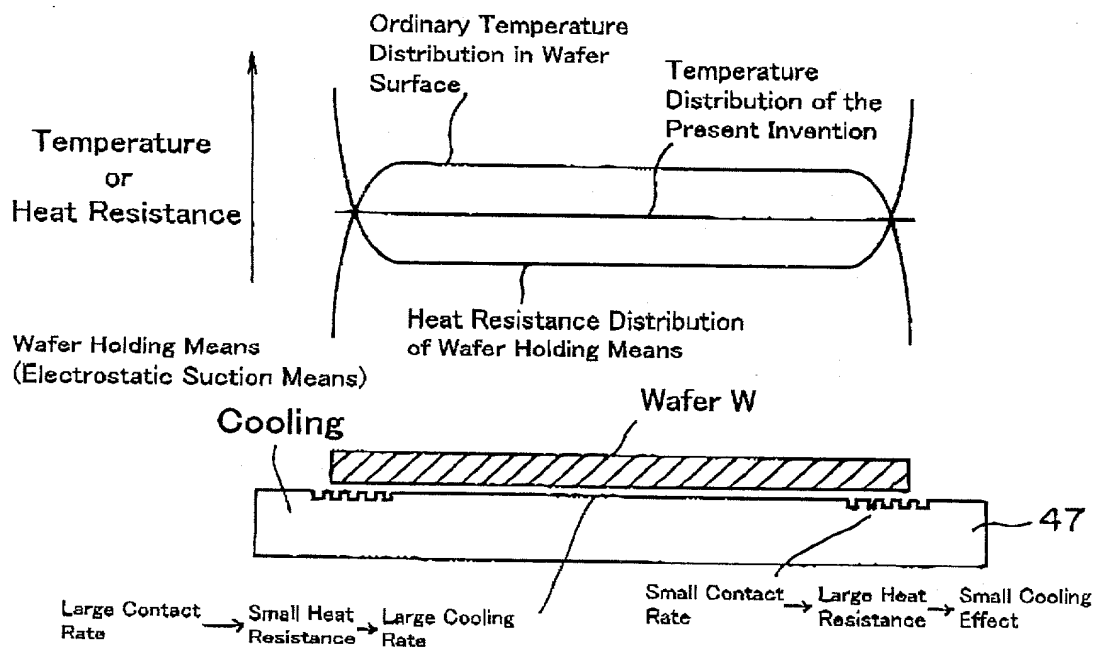
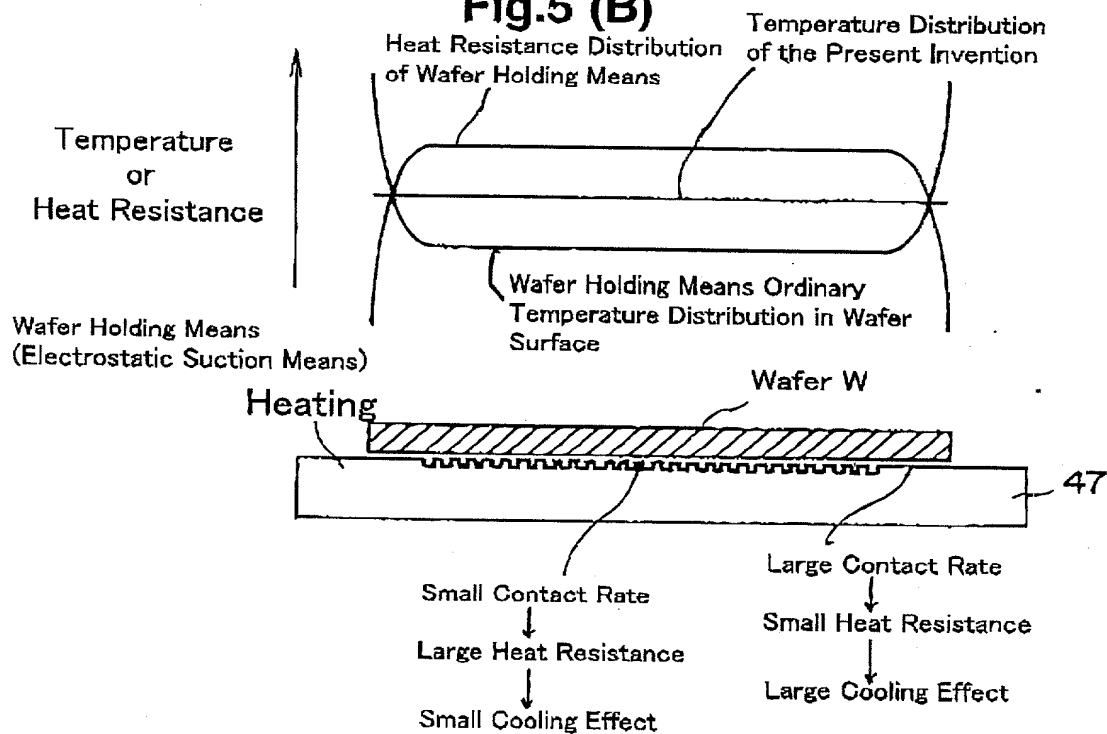


Fig.5 (B)



6/11

Fig.6 (A)

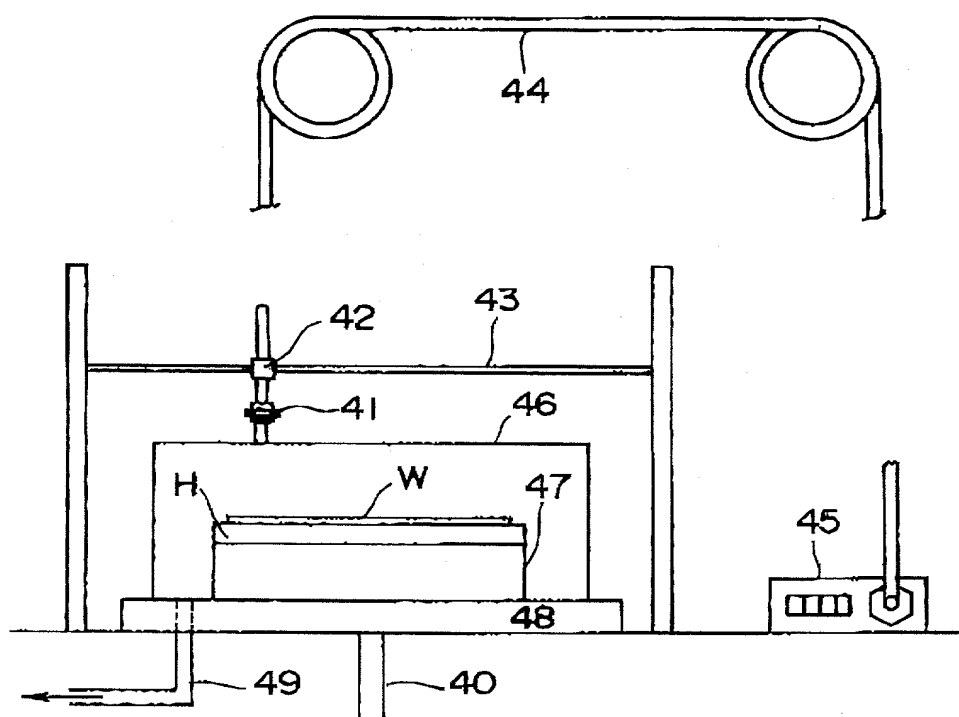
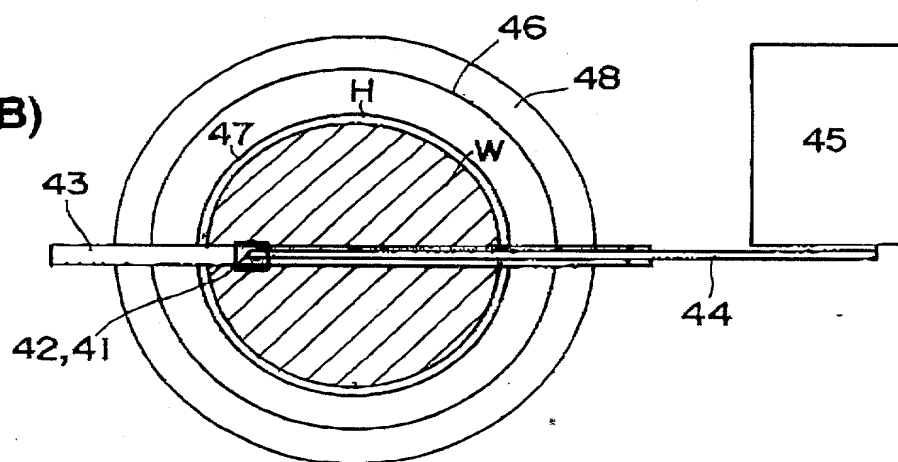


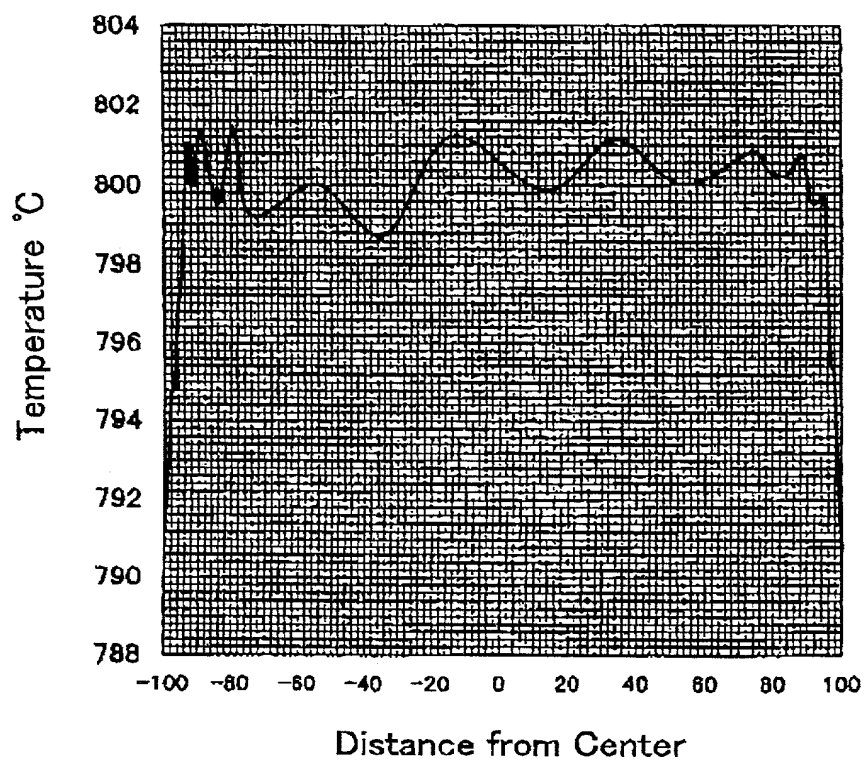
Fig.6 (B)



7/11

Fig.7

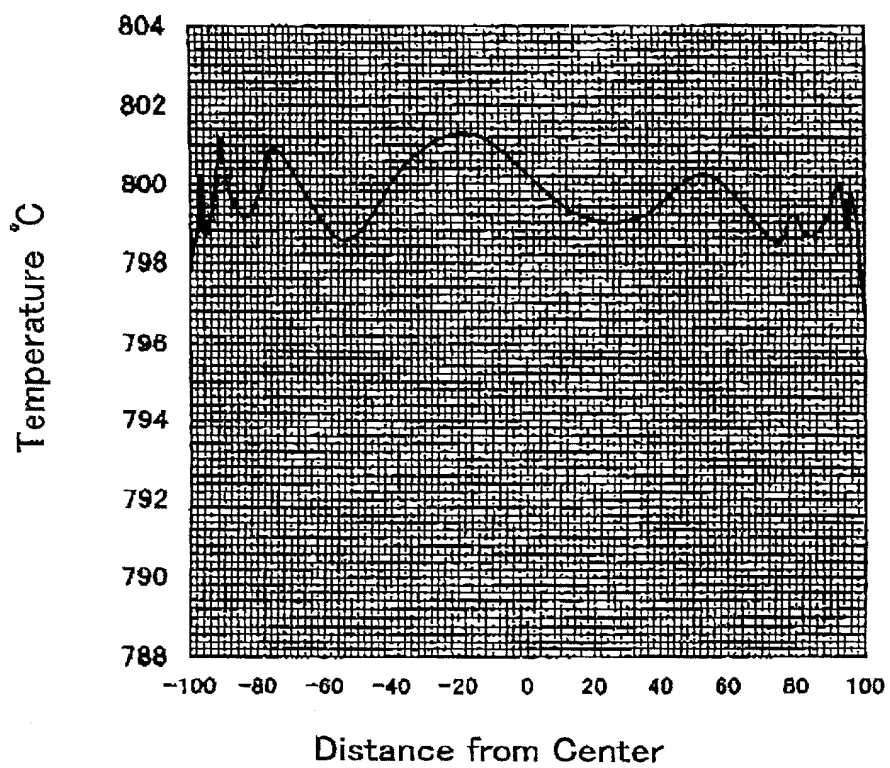
Temperature Distribution in Wafer Surface
Comparative Example



8/11

Fig.8

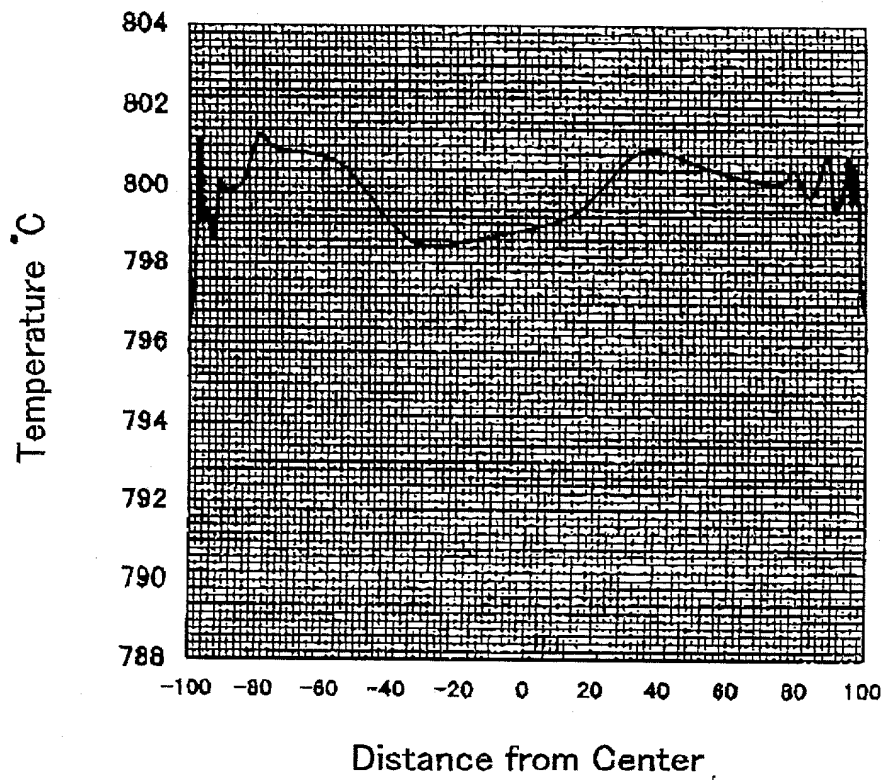
Temperature Distribution in Wafer Surface
Example 1



9/11

Fig.9

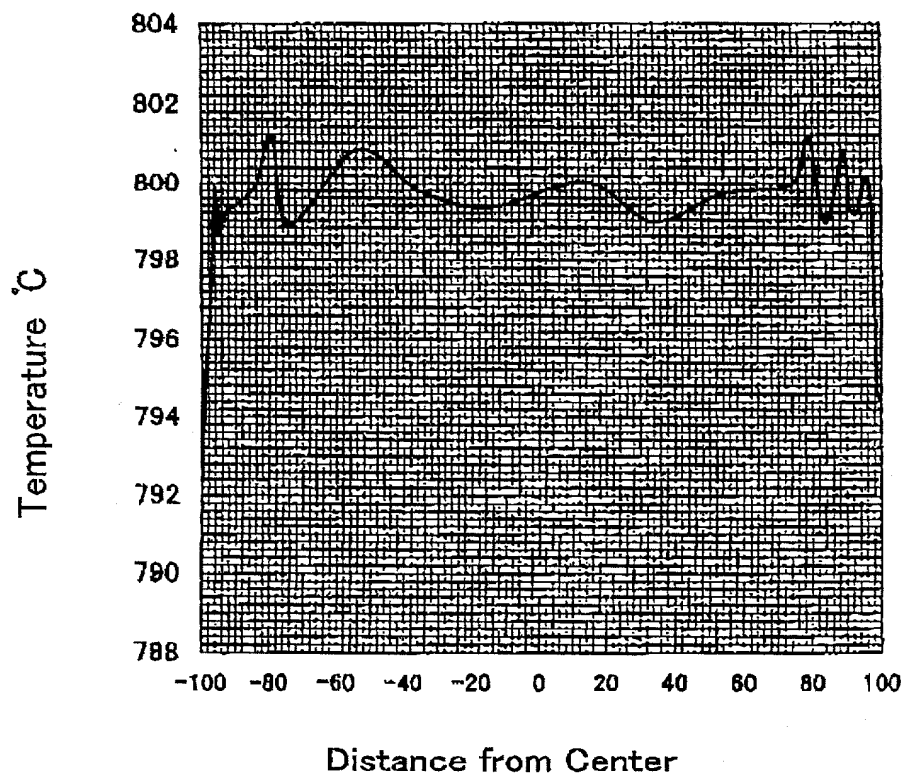
Temperature Distribution in Wafer Surface
Example 2



10/11

Fig.10

Temperature Distribution in Wafer Surface
Example 3



11/11

Fig.11

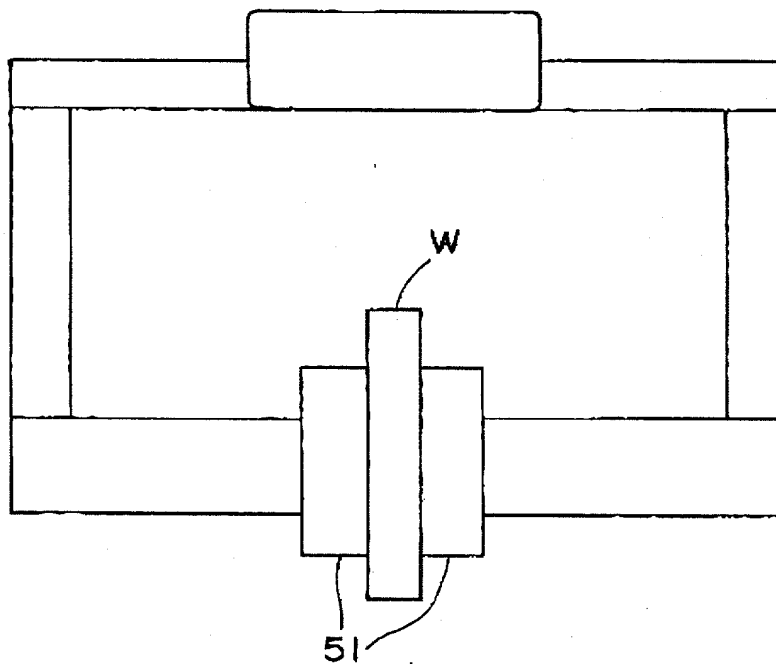


Fig.12

